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Second International Workshop on Network on Chip Architectures, NoCArc , In conjunction with the 42nd Annual IEEE/ACM International Symposium on Microarchitecture (MICRO), December 12, , New York, NY, USA. A paper about this technique entitled "A permutation-based page interleaving scheme to reduce row-buffer conflicts and exploit data locality" was presented and published in the 33rd Annual IEEE/ACM International Symposium on Microarchitecture (Micro, pp. , Monterey, California, December ,).

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